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INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR (IITGN)

Advertisement for JRF Position

19th July, 2019

Job Description

Applications are invited for temporary position of junior research fellow (JRF) for the research project on “Comparative Study of Soft Error Tolerant Synchronous and Asynchronous Processors” funded by Science and Engineering Research Board, Department of Science and Technology, India.

Duration

The total project duration is of 3 years. The candidate will be offered a position for 12 months, which will be extended up to 3 years based on the satisfactory performance of the candidate and availability of funds.

Essential Qualifications

Masters degree in Electrical/Electronics/Electronics and communications/Electronics and Instrumentation/Computer Science and Engineering/Computer Science or related areas.

OR

Bachelor degree in Electrical/Electronics/Electronics and communications/Electronics and Instrumentation/Computer Science and Engineering/Computer Science or related areas with valid GATE score.

Project Description

This project involves design, simulation, implementation and testing of synchronous and asynchronous processor design for space applications. Candidates with excellent programming skills C/C++/Python, Verilog/System Verilog or equivalent, etc. and candidates with interest in computer architecture and having knowledge of system simulators will be given preference. Knowledge of VLSI Design and testing tools will be an advantage.

Application procedure: Interested candidates may apply through the following link:

[Application Form](#)

Interview will be conducted through Skype/In-Person. No TA/DA will be provided for interview.

Note: Applications will be screened until this position is filled