



**Indian Institute of Technology Gandhinagar  
Palaj, Gandhinagar  
Gujarat – 382355**

17/4/2017

## **Advertisement**

Applications are invited from bright, young candidates for the following posts under the SMDP-C2SD project funded by DeITY, Government of India, in Discipline of Electrical Engineering.

- Name of Post : Research Associate
- Consolidated Monthly Pay : Rs. 20,000/- per month consolidated
- Qualification and Experience : B. E./B. Tech. or M. E./M. Tech. in Electrical/Electronics/Computer Science Engineering and related disciplines with a valid GATE score and knowledge in VLSI Design, simulation and testing. The job will include design and simulations of VLSI circuits, use of Cadence, Synopsys tools, Testing of ICs, etc. Knowledge of Linux OS, Verilog/VHDL, Python/C/C++ is an added advantage.
- Tenure : One year

Eligible candidates may apply online (through the following link) on or before 30<sup>th</sup> April 2017. Selection will be based on interview and shortlisted candidates will be intimated well in advance the date and time of the interview.